

IDS

10/648,603

PATENT APPLICATION

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 Applicant: Ki-chul Chun
 Filing Date: August 25, 2003 Gr up: 2815 ~~not yet assigned~~
 For: HALF VOLTAGE GENERATOR FOR USE IN SEMICONDUCTOR MEMORY DEVICE

INFORMATION DISCLOSURE CITATION
 FORM PTO-1449 (Modified)

U.S. PATENT DOCUMENTS

Exam Init	Ref	Document Number	Issue Date	Name	Class	Sub Class
<u>W</u>		5,949,284	9/7/1999	Frisch		
<u>W</u>		6,002,601	12/14/1999	Pappalardo et al.		

330/253
363/71

FOREIGN PATENT DOCUMENTS

Exam Init	Ref	Document Number	Publication Date	Country	Name

OTHER DOCUMENTS

Exam Init	Ref	Author, Title, Date, Pertinent Pages, Etc.)

Examiner: Long Nguyen

Date Considered: 5/19/04